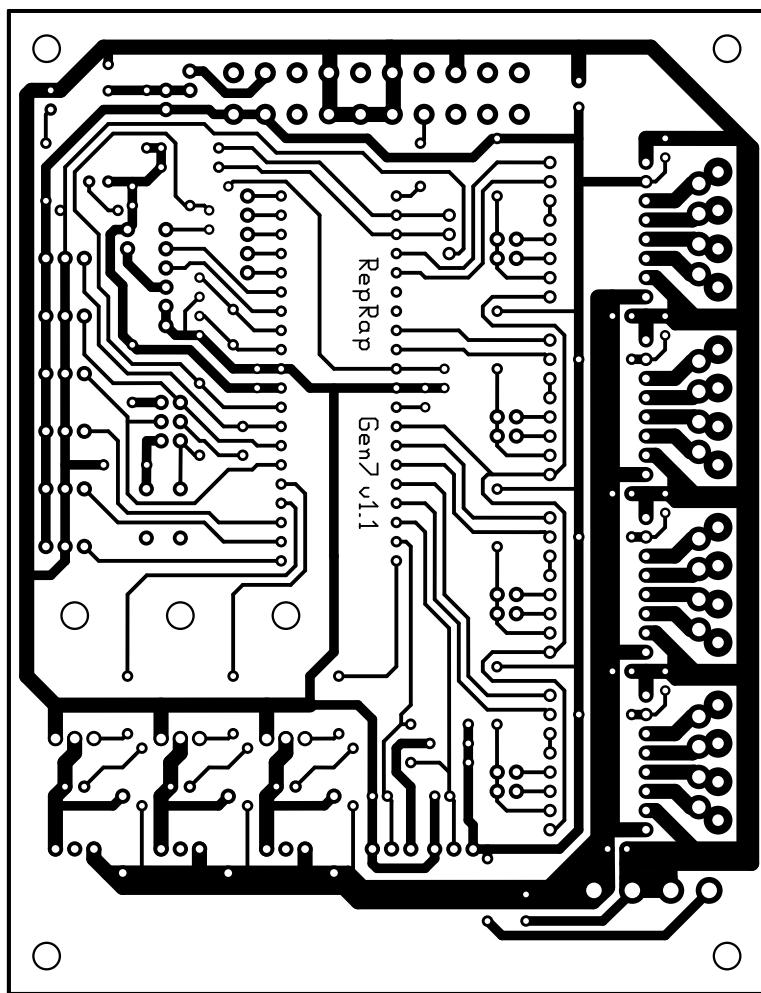
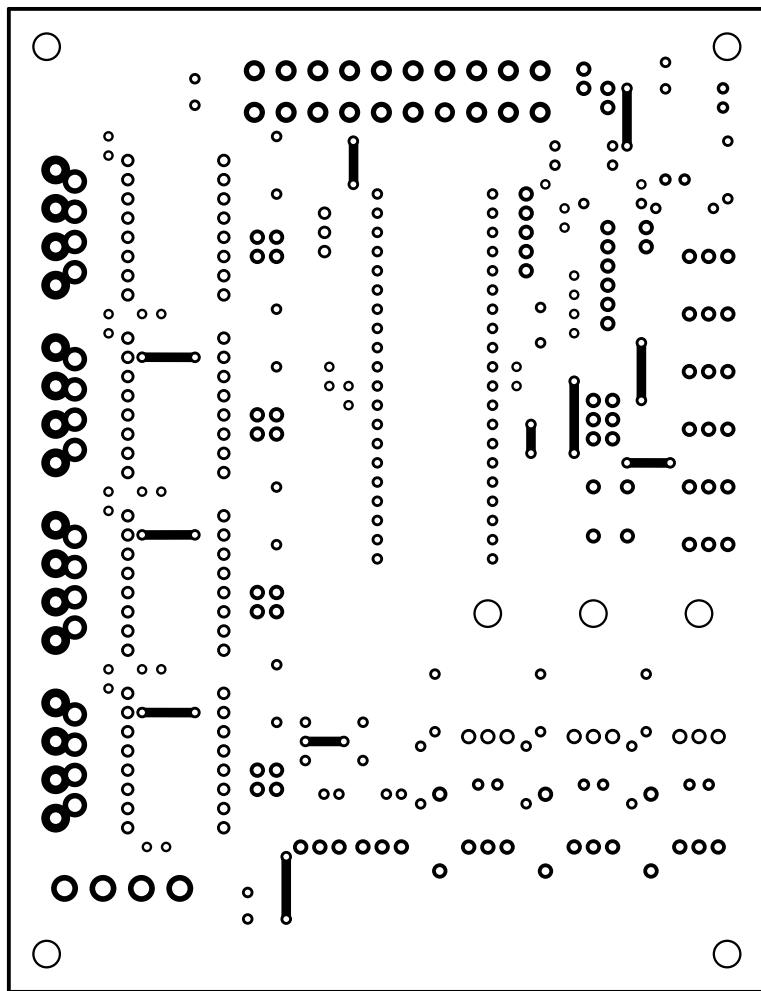


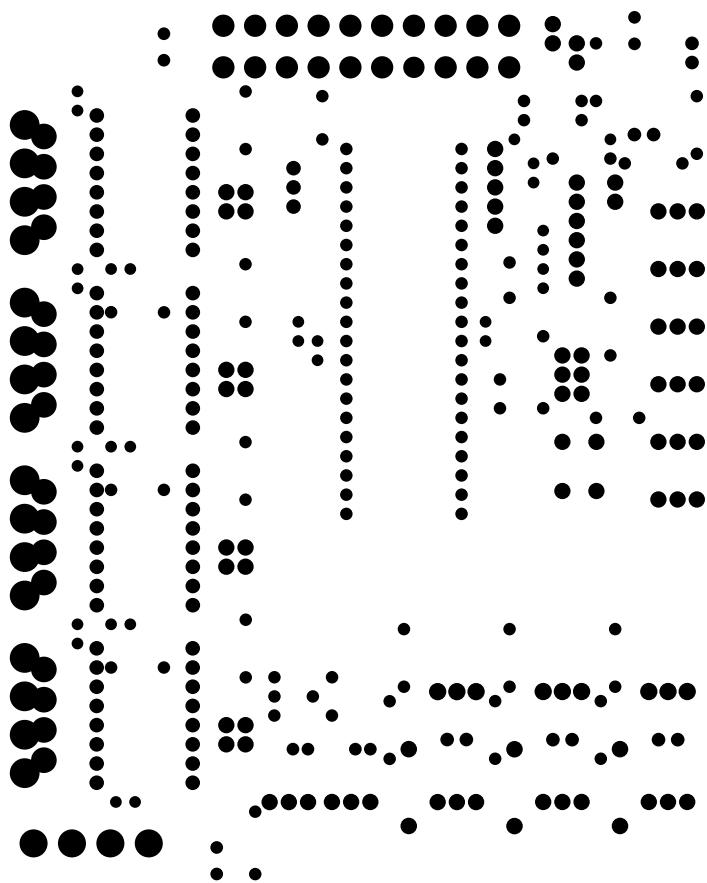
2. solder
3. component
4. componentmask
5. soldermask
6. plated-drill
7. topsilk
8. bottomsilk
9. topassembly
10. bottomassembly
11. fab



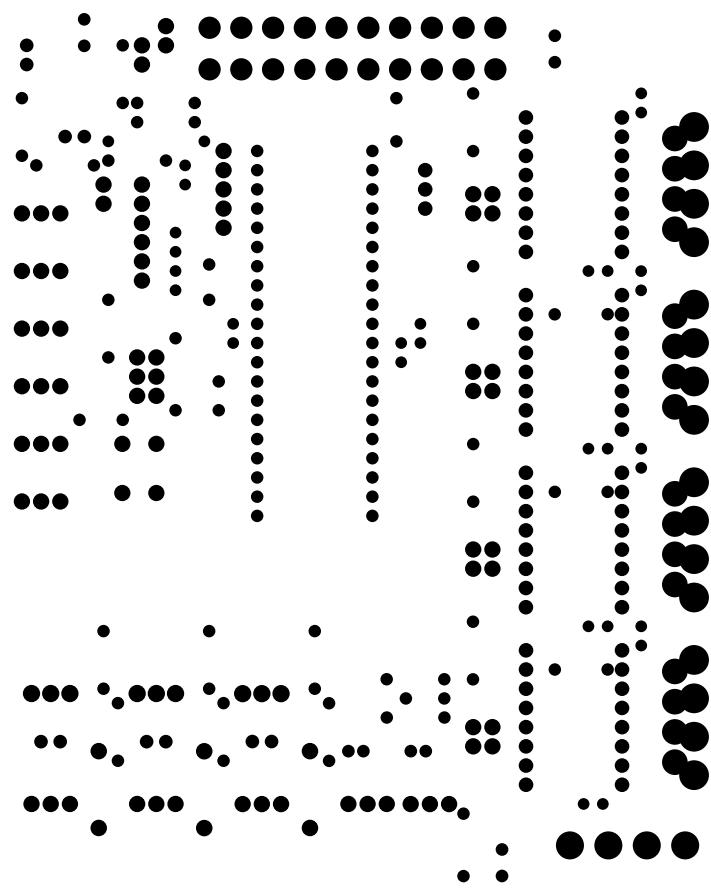
back (mirrored), scale = 1:1.000  
/home/mah/RepRap/Generation\_7\_Electronics/Gen7Board.pcb



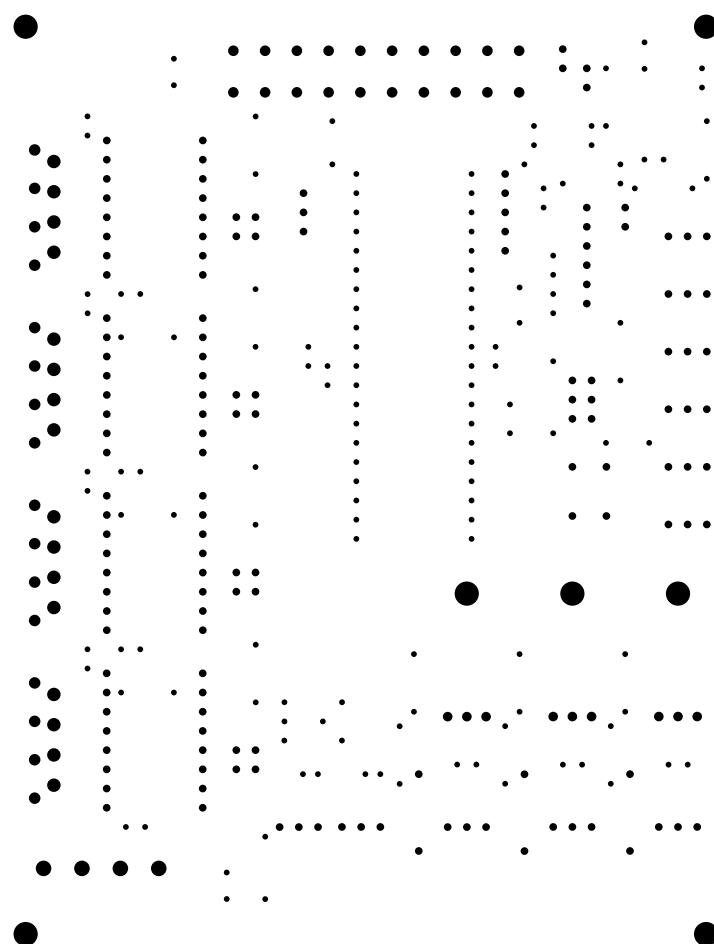
front, scale = 1:1.000  
/home/mah/RepRap/Generation\_7\_Electronics/Gen7Board.pcb



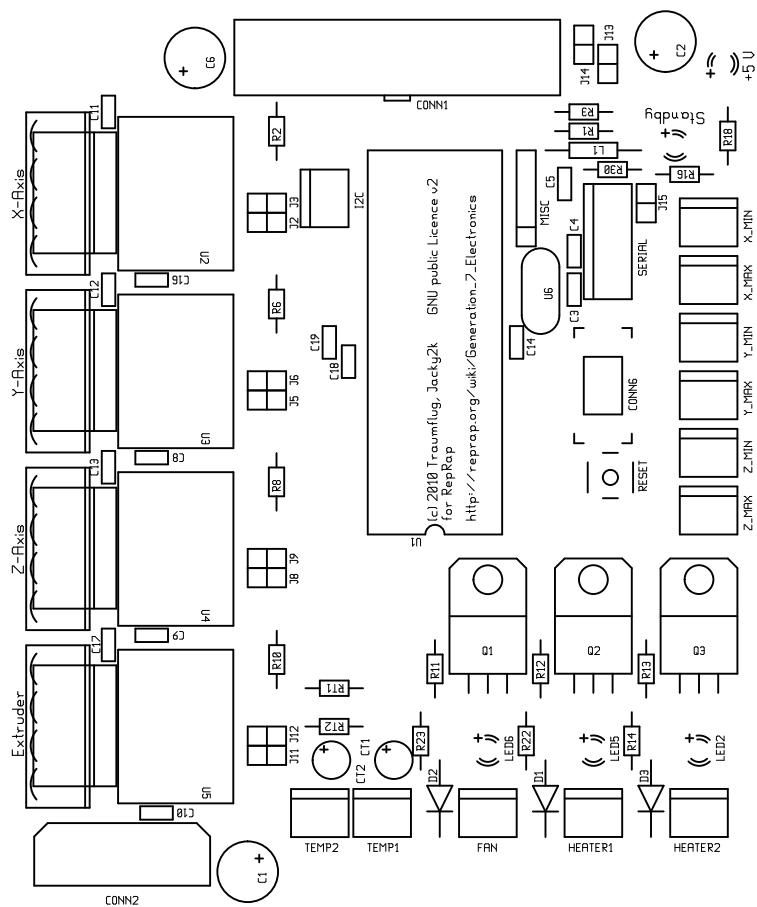
frontmask, scale = 1:1.000  
/home/mah/RepRap/Generation\_7\_Electronics/Gen7Board.pcb



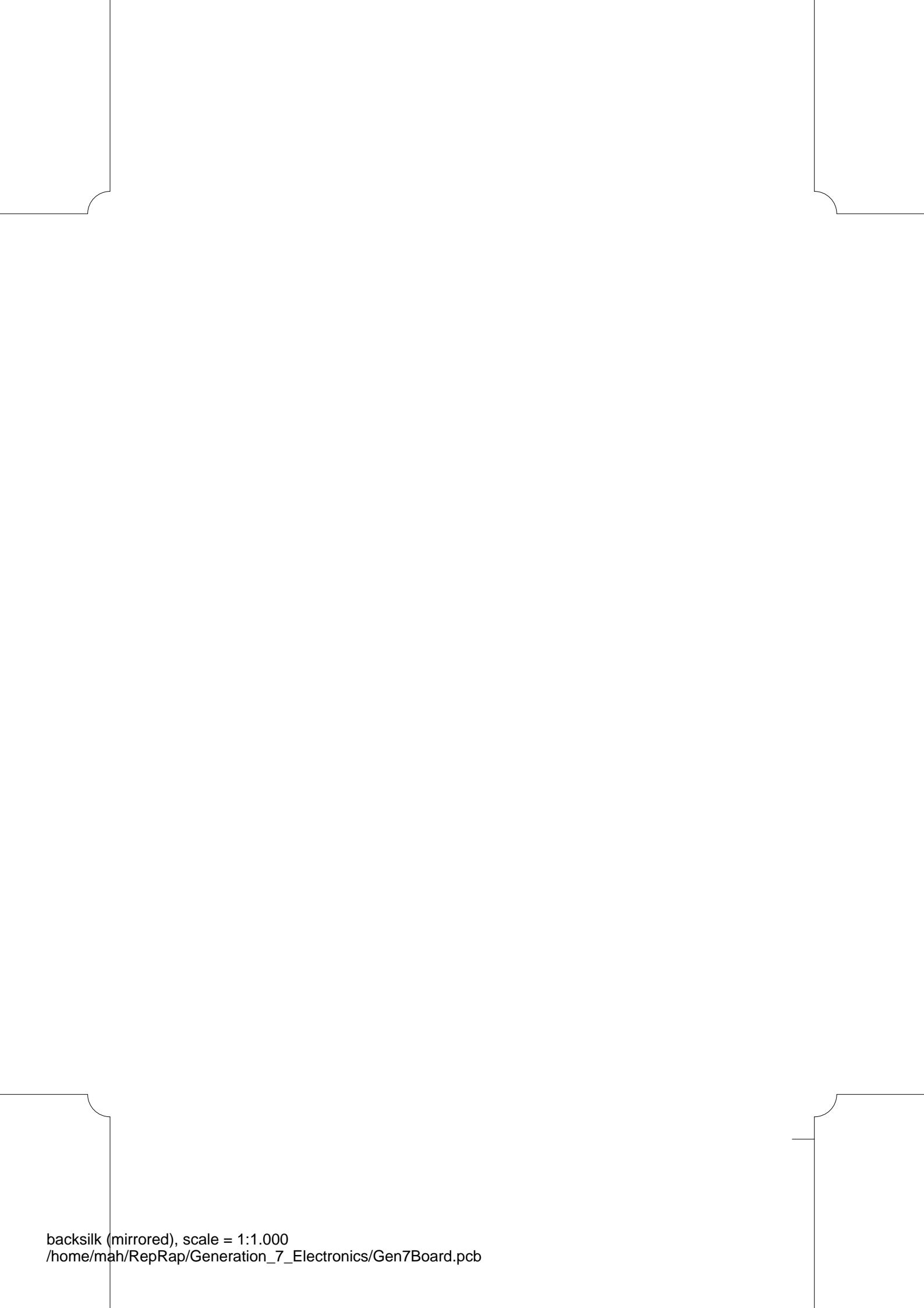
backmask (mirrored), scale = 1:1.000  
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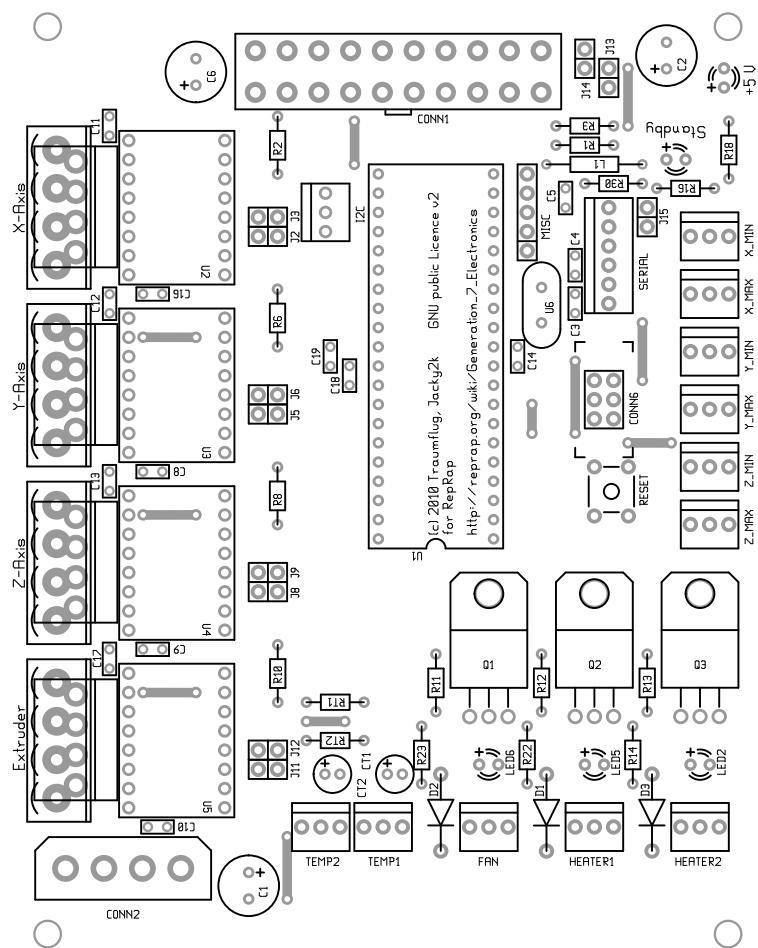
plated-drill, scale = 1:1.000  
/home/mah/RepRap/Generation\_7\_Electronics/Gen7Board.pcb



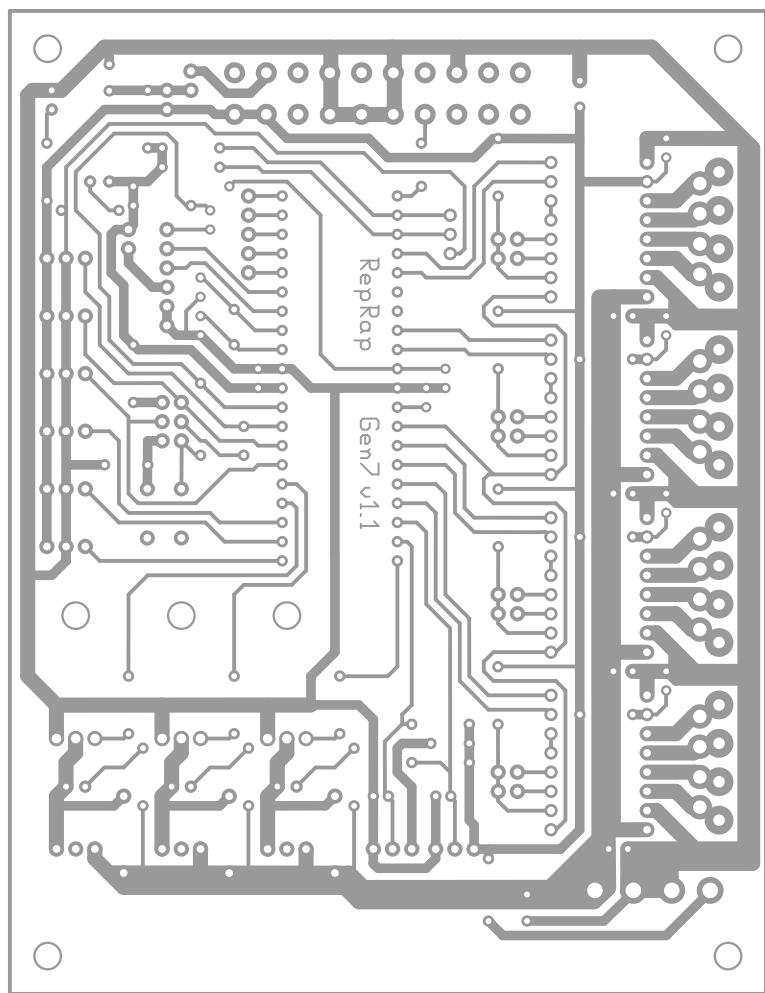
frontsilk, scale = 1:1.000  
 /home/mah/RepRap/Generation\_7\_Electronics/Gen7Board.pcb



backsilk (mirrored), scale = 1:1.000  
/home/mah/RepRap/Generation\_7\_Electronics/Gen7Board.pcb



frontassembly, scale = 1:1.000  
/home/mah/RepRap/Generation\_7\_Electronics/Gen7Board.pcb



backassembly (mirrored), scale = 1:1.000  
/home/mah/RepRap/Generation\_7\_Electronics/Gen7Board.pcb

There are 8 different drill sizes used in this layout, 369 holes total

Symbol	Diam. [Inch]	Count	Plated?
y	0.030	148	YES
+	0.040	149	YES
x	0.050	9	YES
.	0.055	20	YES
o	0.060	16	YES
□	0.070	16	YES
ψ	0.080	4	YES
#	0.081	1	YES
█	0.126	7	YES

Title: (unknown) - Fabrication Drawing

Author: Markus Hitler

Date: Don 17 Mar 2011 19:58:05 GMT UTC

